

ABSTRACT

A method and a base chip monitor the operation of at least one microcontroller unit that is intended for at least one application and is associated with a system to be further developed in such a way as to reliably prevent faulty operation, and particularly faulty operation that discharges the battery. The microcontroller unit has at least one non-volatile memory area associated with it. The memory area can be read from and/or written to by the microcontroller unit, and at least one set of statistics, and in particular a set of fault statistics, relating to the operation of the microcontroller unit, can be kept by means of the memory area.